Dr. Govind Murmu

Assistant Professor Department of Electronics Engineering Indian Institute of Technology (Indian School of Mines), Dhanbad Jharkhand, India - 826 004 Mobile: +91-7870591443 Office: +91-326-223-5123 Email: govind@iitism.ac.in

Education

Ph.D., Electronics Engineering, Indian Institute of Technology (Indian School of Mines) Dhanbad, Jharkhand, India.

M. Tech., Electrical Engineering (Signal Processing and Control), National Institute of Technology, Hamirpur, Himachal Pradesh, India.

B. Tech., Electronics and Communication Engineering, Regional Engineering College, Hamirpur, Himachal Pradesh, India.

PhD Thesis

Transform Domain Adaptive Filters- Performance Study through Simulations and Real-Time Implementation.

Area of Interest

Signal Processing algorithms, Adaptive filters, Blind Source Separation, Beamforming, Adaptive Control Systems.

Professional Experience

Indian Institute of Technology (Indian School of Mines) Dhanbad, India. Assistant Professor, Department of Electronics Engineering, since May 2013.

National Institute of Technology, Hamirpur, Himachal Pradesh, India. Lecturer (On contract), Department of Electrical Engineering, from August 2010 to September 2010.

BSES Rajdhani Power Limited through consultants Safeguards Pvt. Ltd., New Delhi, India

Assistant Manager (Power Supply) from January 2004 to August 2007.

Publications

06 publications in peer reviewed International Journals (SCOPUS indexed). 11 publications in national and international conferences.

PhD guidance

Ongoing-01.

M. Tech. Thesis Guidance

M. Tech. - 12 students in the field of Signal Processing and Communication.

B. Tech. Course taught

Basic Electronics Engineering
Signals and Systems
Signals and Networks
Digital Signal Processing
Digital Image Processing
Digital Communications
Wireless Communication
Digital Signal Processing Lab.
Digital Communication Lab.
Telecommunication and Switching System Lab.
Microprocessor Lab.
Mobile Communication Lab.

M. Tech. Course taught

Advanced Signal Processing Mathematical and Simulation Techniques Digital Image Processing

Membership of professional bodies

Member of IEEE and IEEE Signal Processing Society

Scholarship received

Received the UGC Post Graduate Scholarship for M. Tech. studies during 2008-10.

Received the GATE scholarship for JRF during 2010-13.

Training Done

Summer Faculty Development Program completed at Indian Institute of Technology, New Delhi during summer 2016.

Other Responsibilities

Secretary of the Departmental Advisory Committee (DAC) for ECE department (since October 2019)

Member of Department Post-Graduate Committee(DPGC) since 2020.

Hostel Warden (since July 2020)

Lab-in-Charge of DSP Lab and Communication Lab. in the department (since 2016).

Department web-site coordinator (October 2018-August 2020).

Faculty advisor of B. Tech. ECE students (2016-2020).

Hospitality chair in ICMAP 2018 (a bi-annual international Conference organised by our department).

Tabulator for Department of Electronics Engineering (2014-2017).

Scrutinizer in B. Tech., M. Tech. and Ph.D. admissions (2014-2019).

Member of anti-ragging squad (2018-19).