PUBLICATIONS

Journal publications

1. X. Pan, A. Ghoshal[2], Y. Liu, Q. Xu and A. K. Rathore, "Hybrid Modulation Based Bidirectional Electrolytic Capacitor-less Three-phase Inverter for Fuel Cell Vehicles: Analysis, Design, and Experimental Results" *IEEE Transactions on Power Electronics*, May 2018, Volume: 33, Issue: 5,Pages: 4167 - 4180, DOI:10.1109/TPEL.2017.2718731.
2. V. K. Kanakesh, A. Ghoshal, D. B. Yelaverthy, A. K. Rathore and R. Mahanty, "Analysis and Implementation of Closed Loop Control of Electrolytic Capacitor-less Six Pulse DC Link Bidirectional Three-phase Grid-Tied Inverter," *IEEE Transactions on Industry Applications*, Jan/Feb 2018, Volume: 54, Issue: 1,Pages: 539 - 550, DOI: 10.1109/TIA.2017.2757438.
3. A. Ghoshal, X. Pan and A. Rathore, " Analysis and design of closed loop control of electrolytic capacitor-less six-pulse dc link three-phase inverter." *IEEE Transactions on Industry Applications*, Sept/Oct 2017, Volume: 53, Issue: 5 Pages: 4957 - 4964, DOI:10.1109/TIA.2017.2695543.
4. A. Ghoshal and V. John, "High accuracy multi-rate implementation of resonant integrator using FPGA",*IET Power Electronics*, March 2017, Vol. 10, Issue. 3, pp. 348 - 356, DOI: 10.1049/iet-pel.2015.0702.
5. A. Ghoshal and V. John, "Reconfiguration of three phase MAF-SRF-PLL as single phase PLL", *INAE Letters*, Oct 2016, Vol. 1, Issue. 2, pp. 47 - 51, DOI: 10.1007/s41403-016-0010-8.
6. A. Ghoshal and V. John, " A controller design method for 3 phase 4 wire grid connected VSI with LCL filter", *Sadhana - Academy Proceedings in Engineering Science*, August 2015, Vol. 40, Issue. 5, pp. 1481 - 1499, DOI: 10.1007/s12046-015-0393-3.
7. A. Ghoshal and V. John, " Performance evaluation of three phase SRF-PLL and MAF-SRF-PLL ", *Turkish Journal of Electrical Engineering & Computer Science*, May 2015, Vol. 23, No. 6, pp. 1781 - 1804, DOI:10.3906/elk-1404-488.
8. A. Ghoshal and V. John, " Active damping of LCL filter at low switching to resonance frequency ratio ", *IET Power Electronics*, February 2015, Vol. 8, Issue. 4, pp. 574 - 582, DOI: 10.1049/iet-pel.2014.0355.

Conference publications

1. B. Panda and A. Ghoshal, "Optimal Renewable Energy Resource Allocation by Three Step Optimization," 2019 International Conference on Computing, Power and Communication Technologies (GUCON), NCR New Delhi, India, 2019, pp. 287-293.
2. V. K. Jaiswal and A. Ghoshal,"A design methodology of bidirectional LLC resonant converter for energy storage systems," 2019 IEEE Transportation Electrification Conference and Expo, Asia-Pacific (ITEC Asia-Pacific), Seogwipo-si, Korea (South), 2019, pp. 1-6. doi: 10.1109/ITEC-AP.2019.8903724
3. X. Pan, X. Zhou, Z. Peng, A. Ghoshal, A. K. Rathore, "Novel hybrid modulation based bidirectional electrolytic capacitor-less three-phase inverter for fuel cell vehicles",in 2017 IEEE 3rd International Future Energy Electronics Conference and ECCE Asia (IFEEC 2017-ECCE Asia), Kaohsiung, Taiwan DOI: 10.1109/IFEEC.2017.7992161.
4. A. Ghoshal, A. K. Rathore and Pan Xuewei, "Analysis, design, and implementation of closed loop control of electrolytic capacitor-less six-pulse DC link inverter", in IEEE IAS annual meeting 2016, Portland, USA, Oct 2016, DOI: 10.1109/IAS.2016.7731873.
5. V. K. Kanakesh, A. Ghoshal, D. B. Yelaverthy, A. K. Rathore and R. Mahanty, "High-frequency six pulse dc link based bidirectional three-phase inverter without intermediate decoupling capacitor", in IEEE ECCE 2016, Milwaukee, WI, USA, 18-22 Sept. 2016, DOI: 10.1109/ECCE.2016.7855161.
6. A. Ghoshal and V. John, " DC bus imbalance compensation in a three phase four wire grid connected inverter ", in Proc. Nat. Power Electron. Conf., Shibpur, India, Dec. 2011.
7. A. Ghoshal and V. John, " Operation of current controlled three phase grid connected VSI under non-ideal grid condition", Centenary Conference, 2011 - Electrical Engineering, Indian Institute of Science, Bangalore, India, Dec., 2011.
8. A. Ghoshal and V. John, " Anti-windup schemes for proportional integral and proportional resonant controller ", in Proc. Nat. Power Electron. Conf., Roorkee, India, June 2010.
9. A. Ghoshal and V. John, " A method to improve PLL performance under abnormal grid conditions ", in Proc. Nat. Power Electron. Conf., Bangalore, India, Dec. 2007.