

## **Reviewed International Journal**

### **Year 2024**

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1. Pankaj Kumar, Aasif Mohammad Bhat, Pankaj Kumar Sharma, Rajeev Kumar Ranjan, "A novel and compact MOSFET-C only based grounded meminductor emulator and its application" in *AEU - International Journal of Electronics and Communications*, Volume 183, 2024, 155378, ISSN 1434-8411 <https://doi.org/10.1016/j.aeue.2024.155378>.
2. P. Kumar, R. K. Ranjan and S. -M. Kang, "A Memristor Emulation in 180-nm CMOS Process for Spiking Signal Generation and Chaos Application," in *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 71, no. 4, pp. 1757-1770, April 2024, doi: 10.1109/TCSI.2023.3348695.
3. Tasneem, S.; Ranjan, R.K.; Paul, S.K.; Herencsar, N. Power-Efficient Electronically Tunable Fractional-Order Filter. *Fractal Fract.* **2024**, *8*, 31. <https://doi.org/10.3390/fractalfract8010031>
4. P. Srivastava, R. K. Sharma, R. K. Gupta, F. Kacar and R. K. Ranjan, "New DTMOS Based High Frequency Memristor Emulator and Its Nonlinear Applications," in *IEEE Access*, vol. 12, pp. 9195-9205, 2024, doi: 10.1109/ACCESS.2023.3344311.
5. Surendra Prasad, S., Dutta, S., Choubey, C. K., Dubey, S. K., Priyadarshini, B., & Ranjan, R. K. (2023). Tunable floating and grounded memristor emulator model. *International Journal of Electronics*, 1–18. <https://doi.org/10.1080/00207217.2023.2267218>
6. Niranjana Raj, Rajeev Kumar Ranjan, "Emulation of novel floating and tunable Multimem-Elements circuit and its application" *AEU - International Journal of Electronics and Communications*, Volume 177, 2024, 155215, ISSN 1434-8411, <https://doi.org/10.1016/j.aeue.2024.155215>.
7. Pankaj Kumar sharma, Rajeev Kumar Ranjan, Sung-Mo Kang A Compact Electronically Tunable Meminductor Emulator Model and Its Application *IEEE Circuits & Systems Magazine* IEEE 6.9 2024
8. Sagar, Jagveer Singh Verma, Manoj Joshi, Rajeev Kumar Ranjan, Sung-Mo Kang, A compact memristor emulator for novel IC applications: Its design and experimental validation, *Chaos, Solitons & Fractals*, Volume 183, 2024, 114824, ISSN 0960-0779, <https://doi.org/10.1016/j.chaos.2024.114824>.

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9. P. K. Sharma, S. Tasneem and R. K. Ranjan, "A New Electronic Tunable High-Frequency Meminductor Emulator Based on a Single VDTA," in *IEEE Canadian Journal of Electrical*

and *Computer Engineering*, vol. 46, no. 2, pp. 179-184, Spring 2023, doi: 10.1109/ICJECE.2023.3261886.

10. Tasneem, S., Ranjan, R.K. and Paul, S.K., 2024. Low-Frequency Electronically Tunable Fractional Filter and its Implementation as Neural Network. *Journal of Circuits, Systems & Computers*, 33(2).

11. F. Khateb, M. Kumngern, T. Kulej and R. K. Ranjan, "0.5 V Multiple-Input Multiple-Output Differential Difference Transconductance Amplifier and Its Applications to Shadow Filter and Oscillator," in *IEEE Access*, vol. 11, pp. 31212-31227, 2023, doi: 10.1109/ACCESS.2023.3260146.

12. Sagar, R. K. Ranjan and S. -M. Kang, "Resistorless Floating/Grounded Memristor Emulator Model With Electronic Tunability," in *IEEE Transactions on Circuits and Systems II: Express Briefs*, vol. 70, no. 7, pp. 2340-2344, July 2023, doi: 10.1109/TCSII.2023.3242301.

13. Tasneem, S.; Kumar Sharma, P.; Kumar Ranjan, R.; Khateb, F. Electronically Tunable Memristor Emulator Implemented Using a Single Active Element and Its Application in Adaptive Learning. *Sensors* 2023, 23, 1620. <https://doi.org/10.3390/s23031620>

14. Prashant Kumar, Brajesh Kumar Kaushik, Rajeev Kumar Ranjan, A novel second generation current conveyor (CCII)-based high frequency memristor model, *Microelectronic Engineering*, Volumes 271-272, 2023, 111938, ISSN 0167-9317, <https://doi.org/10.1016/j.mee.2023.111938>.

15. P. Kumar, P. Srivastava, R. K. Ranjan and M. Kumngern, "New Zero Power Memristor Emulator Model and Its Application in Memristive Neural Computation," in *IEEE Access*, vol. 11, pp. 5609-5616, 2023, doi: 10.1109/ACCESS.2023.3236424.

16. Dutta, S., Kumar, P., Ranjan, R.K. *et al.* An Improved DDCCTA Toward its Application in Different Wave-Function and PWM Generation. *Arab J Sci Eng* 48, 14313–14332 (2023). <https://doi.org/10.1007/s13369-022-07559-x>

17. Pankaj Kumar Sharma, Sagar Surendra Prasad, Sadaf Tasneem, Bindu Priyadarshini, Rajeev Kumar Ranjan, "Resistive tunable memristor emulator model and its application, AEU" - International Journal of Electronics and Communications, Volume 160, 2023, 154500, ISSN 1434-8411, <https://doi.org/10.1016/j.aeue.2022.154500>.

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18. Sagar Surendra Prasad, Prashant Kumar, Niranjana Raj, Pankaj Kumar Sharma, Bindu Priyadarshini, Rajeev Kumar Ranjan, Pipat Prommee, "A compact floating and grounded memristor model using single active element", *AEU - International Journal of Electronics and Communications*, Volume 157, 2022, 154426, ISSN 1434-8411, <https://doi.org/10.1016/j.aeue.2022.154426>.

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22. S. Tasneem, R. K. Ranjan and S. K.Paul, "Performance Enhancement of Current Follower Transconductance Amplifier (CFTA) and its Application as Filter" *Journal of Circuits, Systems, and Computers* ISSN 1350-2409, World Scientific, Singapore, 2021.

23. Raj, N.; Sagar; Ranjan, R.K.; Priyadarshini, B.; Bizon, N. Electronically Tunable Full Wave Precision Rectifier Using DVCCTAs. *Electronics* **2021**, *10*, 1262. <https://doi.org/10.3390/electronics10111262>

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29. W. Jaikla, F. Khateb, M. Kumngern, T. Kulej, R. K. Ranjan and P. Suwanjan, "0.5 V Fully Differential Universal Filter Based on Multiple Input OTAs," in *IEEE Access*, vol. 8, pp. 187832-187839, 2020, doi: 10.1109/ACCESS.2020.3030239.

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### **International/National Conferences:**

1. Somenath Dutta, Rajeev Kumar Ranjan, Dharmendra Kumar Singh, "Designing Tuneable Circuits Using the DDCC2TA for Square, Triangular Wave Generation and Pulse Width Modulation". Accepted for publication in Second International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT 2023)
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